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0	A	"Access and Alignment of Data in an Array Processor", by Lawrie, IEEE Trans. on Computers, Vol.c-24, No. 12, December, 1975 pgs. 1145-1155	,		
4	В	"The Knockout Switch: A Simple, Modular Architecture for High- Performance Packet Switching", by Yeh et. al, IEEE Journal on Selected Comm, Vol. SAC-5, No.8, October 1987, pgs. 274-282			
A	С	"Optimal, Multi-Stage Interconnection by Divide-and-Conquer Networks", by Li, Proc. of Second IASTED Conf, December 1998, pgs. 318-323			
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